



501.41215X00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: T. SASAKURA, et al.
Application No.: 10/077,779
Filed: February 20, 2002
For: SEMICONDUCTOR INTEGRATED CIRCUIT MODULES,
MANUFACTURING METHODS AND USAGE THEREOF
Art Unit: 2814
Examiner: D. P. Nguyen

RECEIVED
JAN 13 2004
TIC 2800 MAIL ROOM

AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

January 5, 2004

Sir:

In response to the Office Action dated September 4, 2003, the period of response for which extension is requested by the attached Petition for Extension of Time, please amend the above-identified application as listed below and as set forth on the following pages:

Amendments to the Abstract;

Amendments to the Claims;

Remarks are included following the amendments; and

an APPENDIX I providing a replacement Abstract is attached following the remarks.